

Radio Controlled Clock Receiver

Description

The U4221B is a bipolar integrated straight through receiver circuit in the frequency range of 60 to 80 kHz. The device is designed for radio controlled clock application.

Features

- Low power consumption
- Very high sensitivity
- High selectivity by quartz resonator
- Stop-function available
- Only a few external components necessary
- Digitized serial output signal

Block Diagram

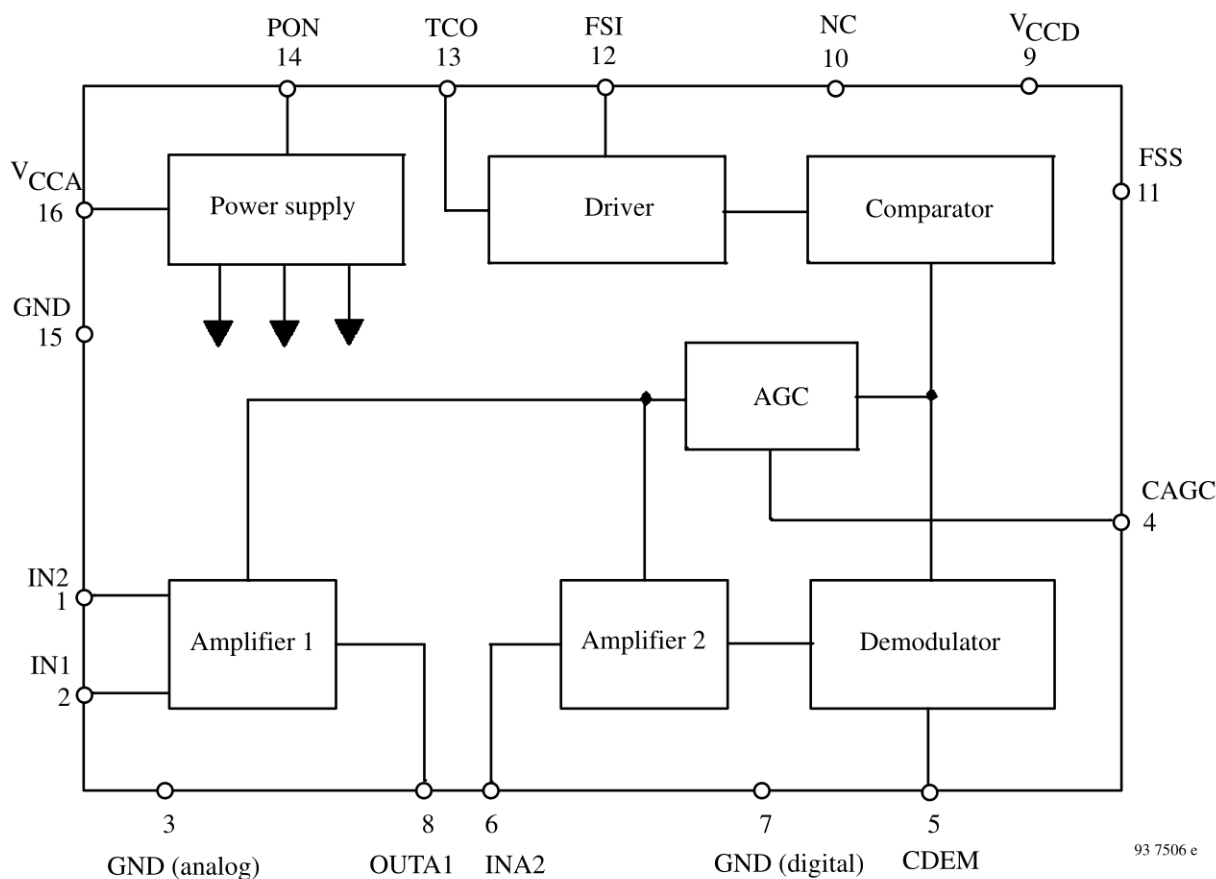
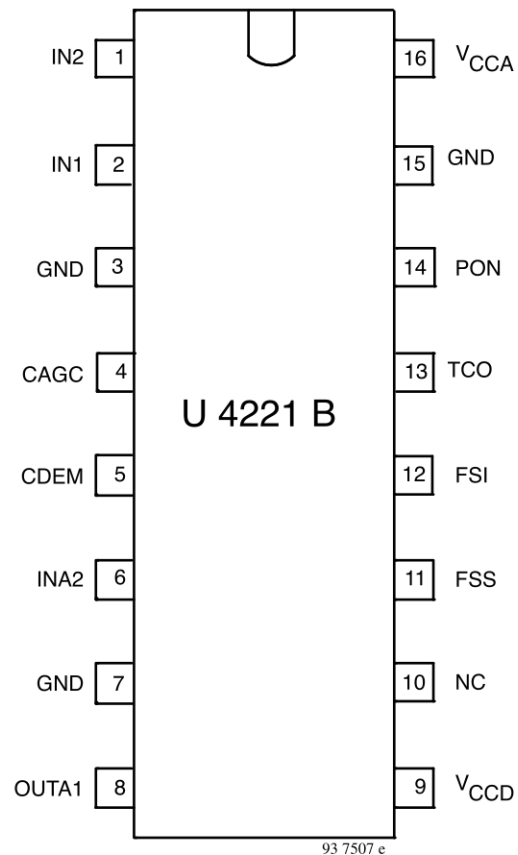


Figure 1.

Pin Description

Pin	Symbol	Function
1	IN2	Amplifier 1 - Input 2
2	IN1	Amplifier 1 - Input 1
3	GND	Analog ground
4	CAGC	Time constant of AGC
5	CDEM	Low pass filter
6	INA2	Amplifier 2 input
7	GND	Digital ground
8	OUTA1	Amplifier 1 output
9	V _{CCD}	Supply voltage (digital)
10	NC	Not connected
11	FSS	Field strength select
12	FSI	Field strength indication
13	TCO	Time code output
14	PON	Power ON/OFF control
15	GND	Ground (substrate)
16	V _{CCA}	Supply voltage (analog)



IN1, IN2

IN2 is connected to Pin 16 (V_{CCA}). A ferrite antenna is connected between IN1 and IN2. Q of antenna circuit should be as high as possible, but the temperature influence must be compensated. The resonant resistance should be 200 kΩ to 300 kΩ for optimal sensitivity.

OUTA1, INA2

To achieve a high selectivity, a quartz resonator is connected between the pins OUTA1 and INA2. It is used with the serial resonance frequency of the time code transmitter (e.g. 60 kHz WWVB, 77.5 kHz DCF). The parasitic parallel capacitance C₀ of the quartz resonator should be 0.5 pF to 1 pF.

CAGC

A control voltage derived from the field strength is generated to control the amplifiers. The time constant of this automatic gain control (AGC) is influenced by the capacitor CAGC.

CDEM

After demodulation the signal is low pass filtered by the capacitor CDEM.

PON

If PON is connected to V_{CCD}, the U4221B receiver IC will be activated. The set-up time is typical 2.5 s after applying V_{CCD} at this pin. If PON is connected to GND, the receiver will go into stop mode.

FSS

This pin is connected to GND, otherwise the field strength indication FSI is disabled.

FSI

If the voltage at the input of amplifier 1 is higher than about 5 μV, FSI will be high.

TCO

The digitized serial signal of the time code transmitter can be directly decoded by a microcomputer. Details about the time code format of several transmitters are described separately.

The output consists of a PNP current source and a NPN switching transistor T_S . The guaranteed source output current is $0.2 \mu A$ (TCO = high) and the sink current is $1 \mu A$ (TCO = low). Considering these output currents, the supply voltage and the switching levels of the following $\propto C$, the lowest load resistance is defined. The maximum load capacitance is 100 pF.

In order to improve the driving capability an external pull-up resistor can be used. The value of the resistor should be $4.7 M\Omega$. To prevent an undefined output voltage in the power-down state of the U4221B, the use of this pull-up resistor is recommended.

An additional improvement of the driving capability may be achieved by using a CMOS driver circuit or a NPN transistor with pull-up resistor connected to the collector (see figure 2). Using a CMOS driver this circuit must be connected to V_{CCD} .

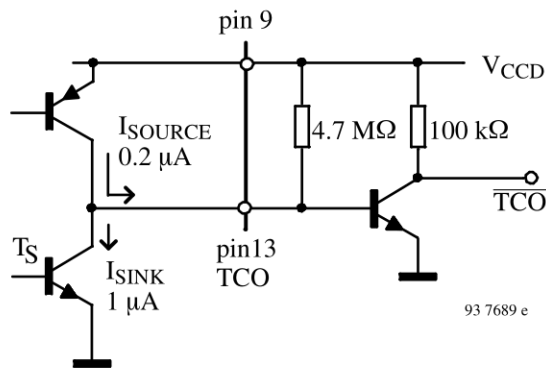


Figure 2.

Functional Description

The following description gives you some additional information and hints in order to facilitate your design, in particular the problems of the antenna.

Figure 3 shows the principal function of the receiver (simplified consideration).

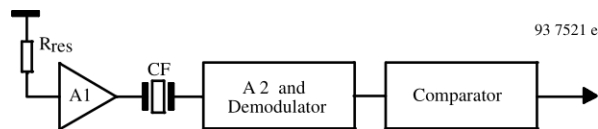


Figure 3.

R_{res} : resonant resistance, A1: preamplifier, A2: amplifier 2, CF: crystal filter

Condition for signal reception:
 $S/N \oplus 4$ at comparator input.

Important parameters are:

$$V_{NA} = (4 k T R_{res})^{1/2}$$

$$BW_A = f_{res}/Q_A$$

input noise voltage density of preamplifier:
 V_{NA1} : 40 nV/Hz^{1/2} (typ)

bandwidth of preamplifier:
 BW_{A1} : 60 kHz (typ)

bandwidth of crystal filter:
 BW_{CF} : 16 Hz (typ)

ultimate attenuation of crystal filter:
 D_{CF} : -35 dB (typ)

whereas:

- V_{NA} antenna noise voltage density
- k $1.38 \cdot 10^{-23}$ Ws/K (Boltzmann constant)
- T absolute temperature
- BW_A bandwidth of antenna
- f_{res} resonant frequency
- Q_A Q antenna

The equivalent input noise voltage at the preamplifier input is:

$$V_N = \sqrt{\left(V_{NA} \cdot \sqrt{BW_{CF}} \right)^2 + \left(\frac{V_{NA} \cdot \sqrt{BW_A}}{D_{CF}} \right)^2 + \dots + \left(V_{NA1} \cdot \sqrt{BW_{CF}} \right)^2 + \left(\frac{V_{NA1} \cdot \sqrt{BW_{A1}}}{D_{CF}} \right)^2}$$

whereas:

$$R_{res} = 300 k\Omega, BW_A = 1 kHz \text{ then } V_N \oplus 0.4 \mu V$$

The condition for signal reception is:
 $S/N \oplus 4 \Rightarrow$ sensitivity $\oplus 1.6 \mu V$

That means that the noise voltage of antenna within the bandwidth of the crystal filter dominates and the bandwidth of antenna is uncritical for the sensitivity aspect.

There is some consideration concerning the calculation of R_{res} :

in order to achieve high signal voltage:

R_{res} should be high

in order to achieve low antenna noise voltage:

R_{res} should be low

$R_{res} < 200 \text{ k}\Omega$:

the input noise voltage of A 1 dominates

$R_{res} > 300 \text{ k}\Omega$:

the antenna noise voltage dominates

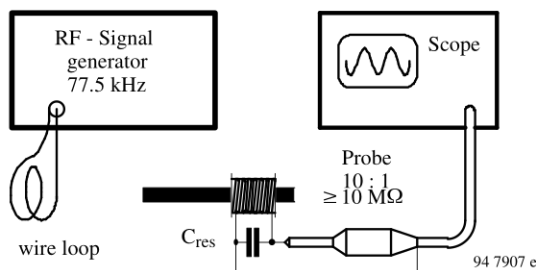
That means the resonant resistance should be between $200 \text{ k}\Omega$ and $300 \text{ k}\Omega$.

Q of antenna must be high for attenuation of interfering signals. But the temperature must not influence the resonance frequency.

Design Hints for the Ferrite Antenna

The bar antenna is the most critical device of the complete clock receiver. But by observing some basic rf design knowledge, no problem should arise with this part. The IC requires a resonance resistance of $200 \text{ k}\Omega$ to $300 \text{ k}\Omega$. This can be achieved by a variation of the L/C-relation in the antenna circuit. But it is not easy to measure such high resistances in the RF region. It is much more convenient to distinguish the bandwidth of the antenna circuit and afterwards to calculate the resonance resistance.

Thus the first step in designing the antenna circuit is to measure the bandwidth. Figure 4 shows an example for the test circuit. The RF signal is coupled into the bar antenna by inductive means, e.g. a wire loop. It can be measured by a simple oscilloscope using the 10:1 probe. The input capacitance of the probe, typically about 10 pF , should be taken into consideration. By varying the frequency of the signal generator, the resonance frequency can be determined.



Afterwards, the two frequencies where the voltage of the rf signal at the probe drops 3 dB down can be measured. The difference between these two frequencies is called the bandwidth BW_A of the antenna circuit. As the value of the capacitor C_{res} in the antenna circuit is well known,

it is easy to compute the resonance resistance according to the following formula:

$$R_{res} = \frac{1}{2 \cdot \pi \cdot BW_A \cdot C_{res}}$$

whereas

R_{res} is the resonance resistance,

BW_A is the measured bandwidth (in Hz)

C_{res} is the value of the capacitor in the antenna circuit (in Farad)

If high inductance values and low capacitor values are used, the additional parasitic capacitances of the coil must be considered. It may reach up to about 20 pF . The Q-value of the capacitor should be no problem if a high Q-type is used. The Q-value of the coil is more or less distinguished by the simple DC-resistance of the wire. Skin effects can be observed but do not dominate.

Therefore it should be no problem to achieve the recommended values of resonance resistance. The use of thicker wire increases Q and accordingly reduces bandwidth. This is advantageous in order to improve reception in noisy areas. On the other hand, temperature compensation of the resonance frequency might become a problem if the bandwidth of the antenna circuit is low compared to the temperature variation of the resonance frequency. Of course, Q can also be reduced by a parallel resistor.

Temperature compensation of the resonance frequency is a must if the clock is used at different temperatures. Please ask your dealer of bar antenna material and of capacitors for specified values of temperature coefficient.

Furthermore some critical parasitics have to be considered. These are shortened loops (e.g. in the ground line of the PCB board) close to the antenna and undesired loops in the antenna circuit. Shortened loops decrease Q of the circuit. They have the same effect like conducting plates close to the antenna. To avoid undesired loops in the antenna circuit it is recommended to mount the capacitor C_{res} as close as possible to the antenna coil or to use a twisted wire for the antenna coil connection. This twisted line is also necessary to reduce feedback of noise from the microprocessor to the IC input. Long connection lines must be shielded.

For the adjustment of the resonance frequency the capacitance of the probe and the input capacitance of the IC are to be taken into account. The alignment should be done in the final environment. The bandwidth is so low that metal parts close to the antenna influence the resonance frequency. The adjustment can be done by pushing the coil along the bar antenna.

Absolute Maximum Ratings

Parameters	Symbol	Value	Unit
Supply voltage	V_{CC}	5.5	V
Ambient temperature range	T_{amb}	-20 to +70	°C
Storage temperature range	R_{stg}	-30 to +85	°C
Junction temperature	T_j	125	°C
Electrostatic handling (MIL Standard 883Y)	$\pm V_{ESD}$	2000	V

Thermal Resistance

Parameters	Symbol	Value	Unit
Thermal resistance	R_{thJA}	70	K/W

Electrical Characteristics

V_{CCA} , V_{CCD} = 3.0 V, reference point Pins 3, 7, 15, input signal according to DCF 77 transmitter, T_{amb} = 25°C, unless otherwise specified

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
Supply voltage range	Pins 9, 16	V_{CCA} V_{CCD}	2.4		5.5	V
Supply current $I_{CC} = I_{CCA} + I_{CCD}$	Pins 9, 16 without reception signal with reception signal > 20 μ V OFF-mode	I_{CC}			40 35 0.2	μ A μ A μ A
Reception frequency range		f_{in}	60		80	kHz
Minimum input voltage	$R_{gen} = 50 \Omega$ Pins 1,2 $R_{res} \leq 300 \text{ k}\Omega$, $Q_{res} > 30$	V_{in}		1.5	1.75	μ V
Maximum input voltage	$R_{gen} = 50 \Omega$ Pins 1,2 $R_{res} \leq 300 \text{ k}\Omega$, $Q_{res} > 30$	V_{in}	40			mV
Input capacitances to ground	Pins 1, 2	$C_{in 1}$ $C_{in 2}$		1 1		pF
Set-up time after POWER ON		t_{pon}		2.5	5	s

TIMING CODE OUTPUT; TCO Pin 13

Output voltage HIGH LOW	$R_{LOAD} = 13 \text{ M}\Omega$ to GND $R_{LOAD} = 2.6 \text{ M}\Omega$ to V_{CCD}	V_{OH} V_{OL}	$V_{CCD}-0.4$		0.4	V V
Output current HIGH LOW	$V_{TCO} = V_{CCD}/2$ $V_{TCO} = V_{CCD}/2$	I_{SOURCE} I_{SINK}	0.2 1		0.4 4	μ A μ A
Decoding characteristics input carrier reduction 100 ms input carrier reduction 200 ms		t_{100} t_{200}	50 150		110 230	ms ms

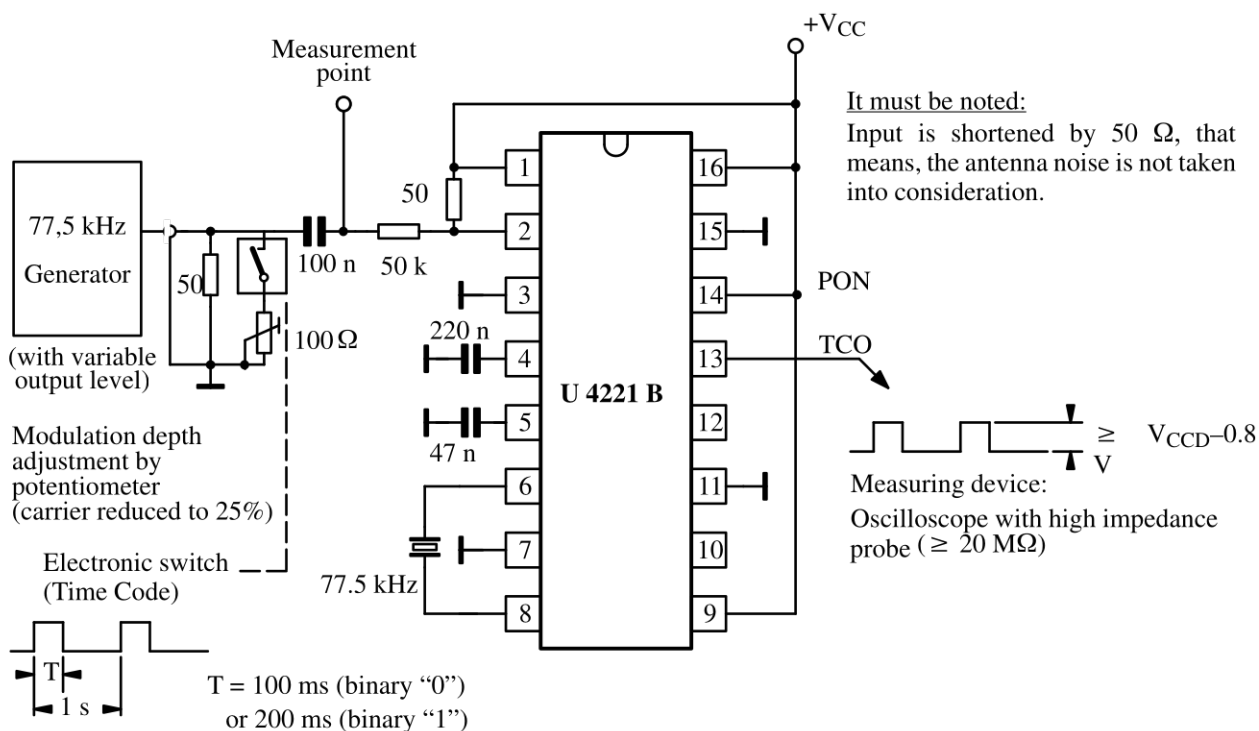
POWER ON/OFF CONTROL; PON Pin 14

Input voltage HIGH LOW	Generator output resistance $\leq 200 \text{ k}\Omega$		$V_{CCD}-0.4$		0.4	V V
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Electrical Characteristics

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
FIELD STRENGTH INDICATION; FSI Pin 12						
Output voltage HIGH	$R_{LOAD} = 13\text{ M}\Omega$ to GND $R_{LOAD} = 2.6\text{ M}\Omega$ to V_{CCD}		$V_{CC}-0.4$		0.4	V
LOW						
Output current HIGH	$V_{TCO} = V_{CCD}/2$		0.2		0.4	μA
LOW	$V_{TCO} = V_{CCD}/2$		1.0		4.0	μA
FIELD STRENGTH SELECT; FSS Pin 11						
Input voltage HIGH	Generator output resistance $\leq 200\text{ k}\Omega$		$V_{CC}-0.4$		0.4	V
LOW						V

Test Circuit for DCF

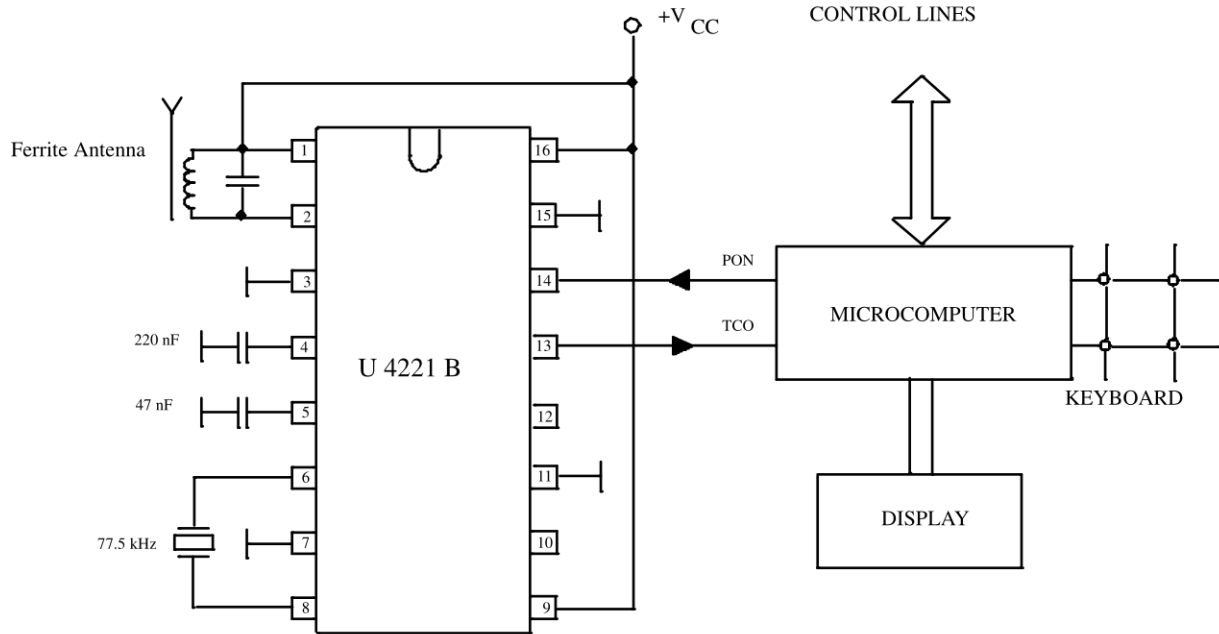


Receiver input signal calibration:

Example: $2\ \mu\text{V}_{\text{eff}}$ input signal $\Rightarrow 2 \cdot 2 \cdot \sqrt{2} \cdot 10^3 = 5.65\text{ mV}_{\text{pp}}$ at measurement point

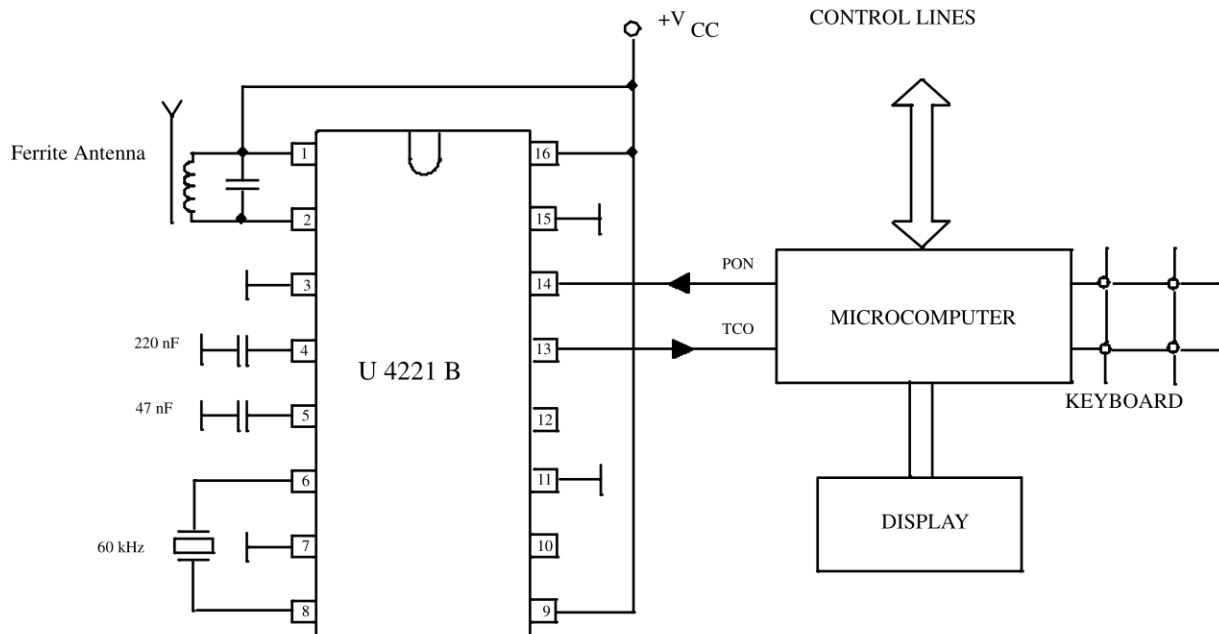
93 7719 e

Application Circuit for DCF 77.5 kHz



93 7504 e

Application Circuit for WWVB 60 kHz

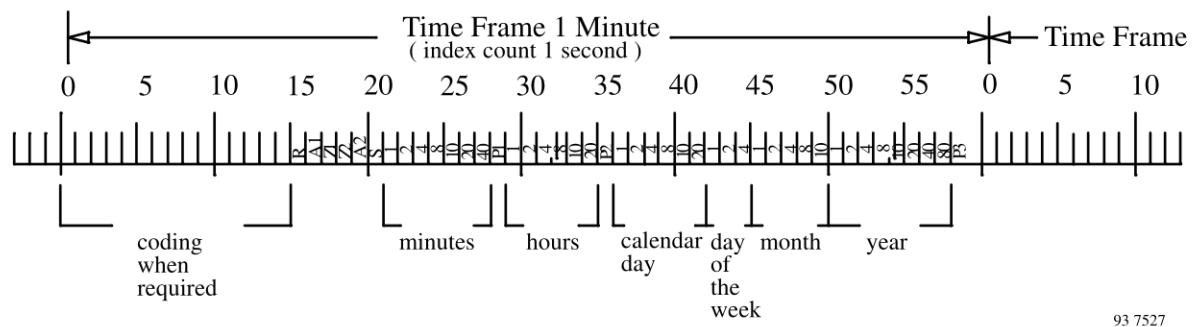


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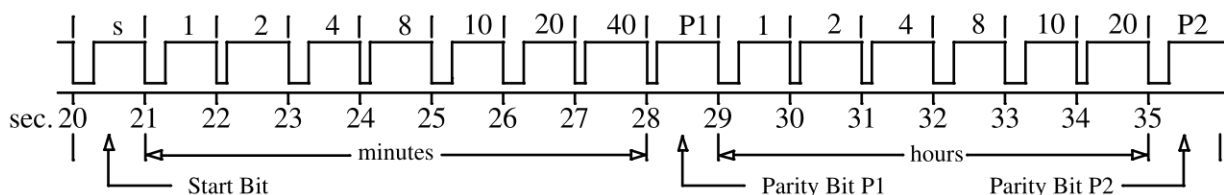
Information Regarding German Transmitter

Station: DCF 77,
 Frequency 77.5 kHz,
 Transmitting power 50 kW

Location: Mainflingen/Germany,
 Geographical coordinates: 50° 01'N, 09° 00'E
 Time of transmission: permanent



Example: 19.35 h



Modulation:

The carrier amplitude is reduced to 25% at the beginning of each second for 100 ms (binary zero) or 200 ms (binary one) duration, excepting the 59th second.

Time Code Format: (based on information of Deutsche Bundespost)

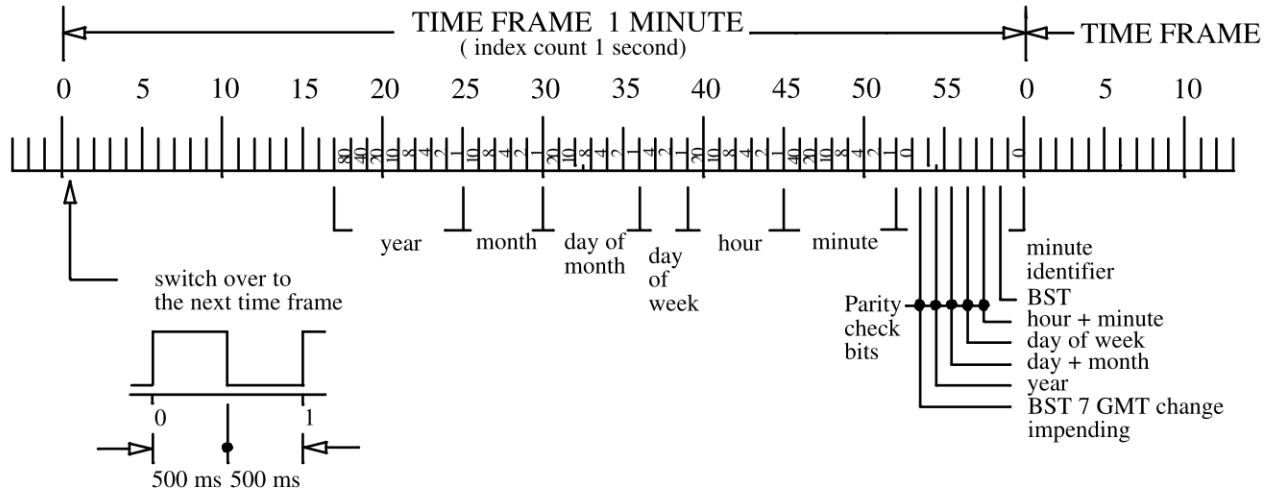
It consists of 1 minute time frames. No modulation at the beginning of the 59th second to recognize the switch over

to the next 1 minute time frame. A time frame contains BCD-coded information of minutes, hours, calendar day, day of the week, month and year between the 20th second and 58th second of the time frame, including the start bit S (200 ms) and parity bits P1, P2 and P3. Further there are 4 additional bits R (transmission by reserve antenna), A1 (announcement of change-over to the summer time), Z1 (during the summer time 200 ms, otherwise 100 ms), Z2 (during standard time 200 ms otherwise 100 ms) and A2 (announcement of leap second) transmitted between the 15th second and 19th second of the time frame.

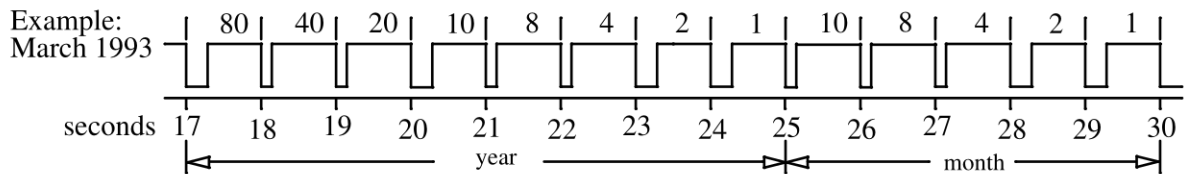
Information Regarding British Transmitter

Station: MSF
 Frequency 60 kHz
 Transmitting power 50 kW
 Location: Teddington, Middlesex

Geographical coordinates: 52° 22'N, 01° 11'W
 Time of transmission: permanent, excepting the first Tuesday of each month from 10.00 h to 14.00 h.



93 7528



Modulation:

The carrier amplitude is reduced at the beginning of each second for the time of 100 ms (binary zero) or 200 ms (binary one).

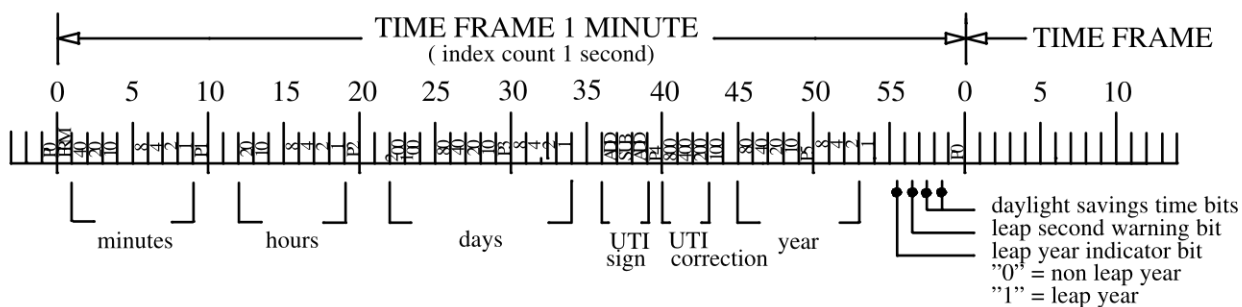
Time Code Format:

It consists of 1 minute time frames. A time frame contains BCD-coded information of year, month, calendar day, day of the week, hours and minutes. At the switch-over to the next time frame, the carrier amplitude is reduced for 500 ms duration.

Information Regarding US Transmitter

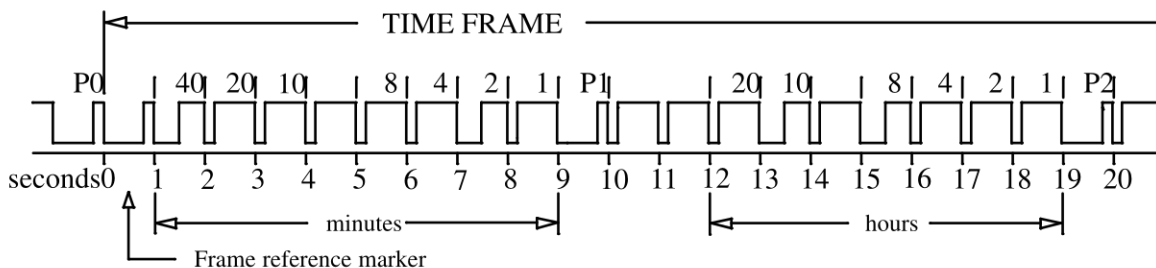
Station: WWVB
 Frequency 60 kHz
 Transmitting power 10 kW

Location: Fort Collins
 Geographical coordinates: 40° 40'N, 105° 03'W
 Time of transmission: permanent.



93 7529 e

Example: UTC 18.42 h



Modulation:

The carrier amplitude is reduced at the beginning of each second and is restored in 500 ms (binary one) or in 200 ms (binary zero).

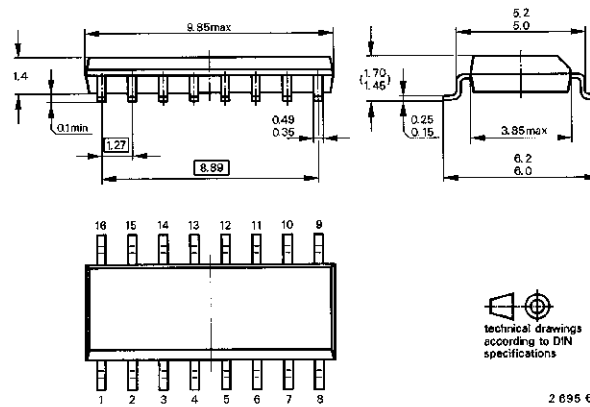
Time Code Format:

It consists of 1 minute time frames. A time frame contains BCD-coded information of minutes, hours, days and year. In addition there are 6 position identifier markers (P0 thru P5) and 1 frame reference marker with reduced carrier amplitude of 800 ms duration.

Ordering and Package Information

Extended Type Number	Package	Remarks
U4221B-BFP	SO16 plastic	
U4221B-BFPG1	SO16 plastic	Taping according to IEC-286-3

Package: SO16



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2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

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